

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Vipin MALIK Keith V. GROESCHEL	§ § §
Serial No.:	Not Yet Assigned	§ Group Art Unit: Unknown
Filed:	Concurrently Herewith	§ Examiner: Unknown
For:	Programmable Logic Device Configuration Via Device Communication Lines	§ § § §

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Att'y. Docket No. 1787-13700  
Client Docket No. M&C020002

Sir:

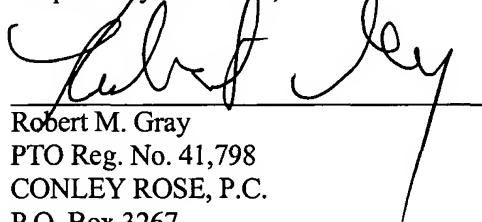
This Information Disclosure Statement comprises a list of pertinent art of which Applicants are aware. A copy of the publications listed on Form PTO-1449 are enclosed herewith.

The submission of this Information Disclosure Statement is not an admission that the art cited is "prior" with respect to the present invention nor is it a representation that no better art exists. Applicants hereby reserve the right to swear behind or otherwise disprove any alleged "prior" nature of any art cited should the facts support and the situation warrant such an action. It is submitted that the art cited does not constitute a bar to the patentability of Applicants' invention.

This Information Disclosure Statement is being filed concurrently with the patent application for the above-identified matter. Therefore, no fee is due for the filing of these papers.

Date: 7/02/03

Respectfully submitted,



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## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use several sheets if necessary)

Atty. Docket No.  
1787-13700

Serial No. NOT  
YET ASSIGNED

Applicant  
Vipin MALIK et al.

Filing Date  
CONCURRENTLY  
HEREWITH

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

**EXAMINER**

**DATE CONSIDERED**

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.